

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF THE CLAIMS:

Claims 1-9 (cancelled)

10. (original) A method of fabricating single electron devices comprising the steps of forming a plurality of particles forming a layer of a thickness sufficiently small to support quantum mechanical tunnelling on the surface of said particles and positioning at least one of said particles between a pair of electrodes to form a single electron device.

11. (original) A method of fabricating single electron devices according to claim 10 characterised in that a further step of selecting particles of predetermined size takes place prior to the step of forming said layer.

12. (currently amended) A method of fabricating single electron devices according to claim 10 ~~or 11~~

characterised in that said plurality of particles is formed as an aerosol.

13. (currently amended) A method of fabricating single electron devices according to claim 10 ~~or 11~~ characterised in that said layer is formed by the chemical modification of the surface of said particles.

14. (currently amended) A method of fabricating single electron devices according to claim 10 ~~or 11~~ characterised in that said layer is formed by the expitaxial deposition of a material on the surface of said particles.

15. (original) A method of fabricating single electron devices according to claim 10 characterised in that the positioning of said particle is performed by means of an atomic force microscope.

16. (original) A method of fabricating a single electron device comprising the steps:
forming a plurality of particles;

forming on the surface of each particle a peripheral layer of a thickness sufficiently small to support quantum mechanical tunnelling therethrough;

providing a pair of electrodes and positioning at least one of said particles between said pair of electrodes to form a single electron device.

17. (original) A method of fabricating a single electron device according to Claim 16, wherein a further step of selecting particles of predetermined size takes place prior to the step of forming said peripheral layer.

18. (original) A method of fabricating a single electron device according to Claim 16, wherein said plurality of particles is formed as an aerosol.

19. (original) A method of fabricating a single electron device according to Claim 16, wherein said peripheral layer is formed by chemical modification of the surface of each of said particles.

20. (original) A method of fabricating a single electron device according to Claim 16, wherein said

peripheral layer is formed by the epitaxial deposition of a material on the surface of each of said particles.

21. (original) A method of fabricating a single electron device according to Claim 16, wherein the positioning of said particle is performed by means of an atomic force microscope.

22. (original) A method of forming a single electron tunnelling device comprising:

forming a particle of a material having a first conductivity characteristic, forming on the particle a semiconducting surface layer of a second conductivity characteristic, the thickness of said layer being sufficiently small to support quantum mechanical tunnelling therethrough; and

positioning said particle between first and second electrodes to provide a current path between the electrodes.

23. (original) A method of forming a single electron tunnelling device, comprising:

forming a particle of a material having a first conductivity characteristic;

forming on the surface of the particle a surface layer of gallium arsenide, the thickness of said layer being sufficiently small to support quantum mechanical tunnelling therethrough; and

positioning said particle between first and second electrodes to provide a current path therebetween.

24. (original) A method of forming a single electron tunnelling device, comprising:

forming a particle of a material having a first conductivity characteristic;

forming on the surface of the particle a peripheral layer of indium oxide, the thickness of said layer being sufficiently small to support quantum mechanical tunnelling therethrough;

positioning said particle between first and second electrodes to provide a current path therebetween.

25. (original) A method of forming a single electron tunnelling device, comprising:

forming a particle of a material having a first conductivity characteristic;

forming on the surface of the particle a peripheral layer of indium arsenide phosphide, the thickness of said

layer being sufficiently small to support quantum mechanical tunnelling therethrough;

positioning said particle between first and second electrodes to provide a current path therebetween.

26. (original) A method of forming a single electron tunnelling device, comprising:

forming a particle of a material having a first conductivity characteristic;

forming on the surface of the particle a peripheral layer of silica, the thickness of said layer being sufficiently small to support quantum mechanical tunnelling therethrough;

positioning said particle between first and second electrodes to provide a current path therebetween.

27. (original) A method forming an electronic device, comprising:

forming at least one nanoparticle having an inner core of a conductive material of predetermined size of nanometer dimensions;

forming on the inner core, an outer shell of a controlled thickness of nanometer dimensions and of a

further material which is different from that of the inner core; and

providing first and second electrodes, and providing a current flow path therebetween comprising said at least one particle, the characteristics of current flow in the current flow path being determined by electron tunnelling via said outer shell and inner core.

28. (original) A method according to Claim 27, wherein said further material is insulating.

29. (original) A method according to Claim 28, wherein said further material is an oxide of one of: silicon, indium, aluminium.

30. (original) A method according to Claim 27, wherein said further material is semiconducting.

31. (original) A method according to Claim 30, wherein said semiconducting material contains one of the following:

indium, silicon.

32. (original) A method according to Claim 27,
wherein said conductive material contains one of:
silicon, germanium, indium, gallium.

33. (original) A method according to Claim 27,
comprising providing a multiplicity of said nanoparticles
stacked adjacent one another whereby to provide said
current flow path.

34. (original) A method of forming a nanocrystal in
the form of a particle that is defined by a size of
nanometer dimensions, the method comprising:

forming, in an aerosol, a core particle of an
electrically conductive material and having a size of
predetermined nanometer dimensions; and

forming epitaxially on the core particle, by the
action of gas on the aerosol, an outer shell of a further
material that is different from that of the core, and
having a controlled thickness of nanometer dimensions.

35. (original) A method according to Claim 34
wherein said conductive material contains one of:
silicon, germanium, indium, gallium.

36. (original) A method according to Claim 34, wherein said further material is semiconducting.

37. (original) A method according to Claim 34, wherein said conductive material contains one of the following:

indium, germanium, gallium;

and said further material is semiconducting and comprises one of the following:

indium, silicon, aluminium.

38. (original) A method of forming a nanocrystal in the form of a particle that is defined by a size of nanometer dimensions, the method comprising:

forming in an aerosol, a core particle of an electrically conductive material and having a size of predetermined nanometer dimensions; and

exposing the gas to an aerosol, the gas reacting with the surface of the core particle to form an outer shell of a further material that is different from the material of the core particle, and that has a controlled thickness of nanometer dimensions.

39. (original) A method according to Claim 38, wherein the gas reacts with the surface of the core particle to form an oxide of the core material.

40. (original) A method according to claim 39, wherein the core particle contains one of: silicon, indium.

41. (original) A method according to Claim 38, wherein the gas reacts with the surface of the core particle by an exchange process, wherein atoms in the surface of the core particle are exchanged for atoms in the gas.

42. (original) A method according to Claim 41, wherein the core material is a compound semiconductor, and the gas comprises molecules containing phosphorus.

43. (original) A method according to claim 42, wherein the core material comprises indium arsenide, and arsenic atoms are replaced by phosphorus atoms.

44. (original) A method of forming a nanocrystal in the form of a particle that is defined by a size of nanometer dimensions, the method comprising:

forming from an aerosol, a core particle of an electrically conductive material and having a size of predetermined nanometer dimensions; and

forming epitaxially on the core, by the action of gas on the aerosol, an outer shell of a further material that is different from that of the core, and having a controlled thickness of nanometer dimensions; and

reacting the outer shell to form an oxide of the further material.

45. (original) A method according to Claim 44, wherein the conductive material is gallium arsenide, the further material is aluminium arsenide, and said oxide is aluminium oxide.

46. (original) A method of forming nanocrystals comprising:

- a) forming an aerosol of particles of a predetermined conductive material and diameters of nanometer dimensions;
- b) filtering said aerosol of particles to provide particles with a narrow predetermined spread of diameters; and

c) processing the filtered aerosol with a vapour of a material in order to form a shell of a further material on each aerosol particle and of a controlled thickness, said further material being different from said predetermined conductive material.

47. (original) A method according to Claim 46, wherein said processing in step c) comprises forming by an epitaxial process said shell.

48. (original) A method according to Claim 47, wherein said conductive material contains one of:
indium, germanium, gallium;
and said further material includes one of:
indium, silicon, aluminium.

49. (original) A method according to Claim 46, including the further step of reacting the further material of the outer shell to form an oxide.

50. (original) A method according to Claim 49, wherein said inner core comprises gallium arsenide, said further material comprises aluminium arsenide, and said oxide comprises aluminium oxide.

51. (original) A method according to Claim 46, wherein in said step c) said vapour reacts with the surface of the particle in order to form said shell by modification of the surface of the particle.

52. (original) A method according to Claim 51, wherein the modification is an exchange process.

53. (original) A method according to claim 51, wherein the particle material is a compound semiconductor, and the vapour comprises molecules containing phosphorus.

54. (original) A method according to claim 53, wherein the particle material comprises indium arsenide, and arsenic atoms are replaced by phosphorus atoms so that the material of the shell is indium arsenide phosphide.

55. (original) A method according to Claim 51, wherein the modification is formation of an oxide.

56. (original) A method according to Claim 55, wherein the material of the particle is one of: silicon, indium.

57. (original) A method according to claim 46, wherein said step a) comprises:

forming an aerosol of group III metallic particles, and filtering the aerosol to provide particles with a narrow predetermined dimensional spread; and

reacting the aerosol with a group V precursor gas, in order to provide an aerosol of particles consisting of III-V semiconductor material.

58. (new) A method of fabricating single electron devices according to claim 11 characterised in that said plurality of particles is formed as an aerosol.

59. (new) A method of fabricating single electron devices according to claim 11 characterised in that said layer is formed by the chemical modification of the surface of said particles.

60. (new) A method of fabricating single electron devices according to claim 11 characterised in that said layer is formed by the epitaxial deposition of a material on the surface of said particles.